Free scale lab

The Freescale Lab is a testament to the power of collaboration between academia and industry. It has been setup in the Department of Electronics and Communication Engineering by NXP Semiconductors, a global semiconductor company which provides solutions that enable secure connections for a smarter world. The key objectives of the lab include bridging the gap between academia and industry, fostering innovation, product, and skill development. By aligning the aims of the lab with the needs of students and research scholars, it creates a fertile ground for innovation, skill development, and meaningful contributions to the semiconductor industry. The lab aims to continually innovate and develop basic semiconductor products and solutions that address emerging technology trends and market needs. It provides students with hands-on experience in the semiconductor industry like improvement of the power efficiency and performance of various devices. This helps the students to gain valuable skills that are highly sought after by employers, enhancing their employability. In a world where practical experience and innovation are highly prized, industry-sponsored labs like the free scale lab offer a promising future for students and research scholars, making them valuable contributors to the technology landscape.

Free scale lab is an industry sponsored lab. It is sponsored by NXP Semiconductors. The company has provided various kits for early level experiments. The objective of the lab is Innovation and Product Development. Lab aims to continually innovate and develop basic semiconductor products and solutions that address emerging technology trends and market needs. This includes improving performance, power efficiency, and integration of products.

Department of Electronics and Communication Engineering
DELHI TECHNOLOGICAL UNIVERSITY

DTU FREE SCALE SYSTEM LABORATORY

Lab Incharge
Prof. Neeta Pandey

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Hardware and Software Available

Software

Vivado Design Suite is a software suite for synthesis and analysis of hardware description language (HDL) designs, superseding Xilinx ISE with additional features for system on a chip development and high-level synthesis. Vivado represents a ground-up rewrite and re-thinking of the entire design flow (compared to ISE).

Hardware

1. HCS12 Microcontroller kit

2. MATLAB embedded trainer

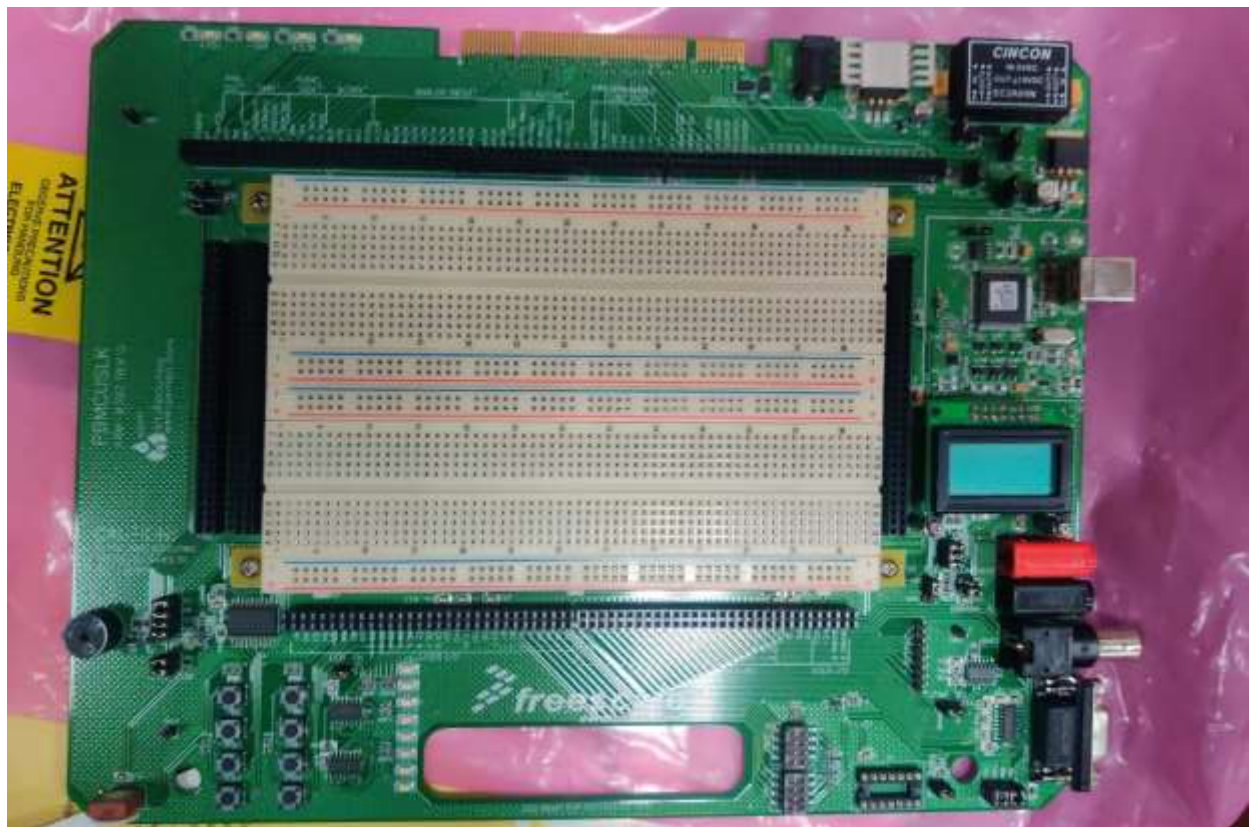3. Window CE.NET based embedded trainer

4. Development kit platform

5. RF transceiver module

6. Student learning kit

7. Embedded kit with ARM9AT91RM9200